

Abstracts

Graphic Design of Matching and Interstage Lossy Networks for Microwave Transistor Amplifier

J.C. Villar and F. Perez. "Graphic Design of Matching and Interstage Lossy Networks for Microwave Transistor Amplifier." 1985 *Transactions on Microwave Theory and Techniques* 33.3 (Mar. 1985 [T-MTT]): 210-215.

A graphical design method for lossless and lossy gain-compensating networks is presented, and the advantages of the use of this technique in the construction of microwave transistor amplifiers are discussed. The method is based on the use of a set of constant S_{ji} circles plotted on the Smith Chart, and is easily automated on a personal computer. Two applications are presented: a two-stage amplifier with 20 dB in the 100-1100-MHz band and an ultra-broad-band lossy matched amplifier stage with 4 dB in the dc-16-GHz band.

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